

Abstracts

Characterization of asymmetric coupled CMOS lines

*U. Arz, D.F. Williams, D.K. Walker, J.E. Rogers, M. Rudack, D. Treytnar and H. Grabinski.
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This paper investigates the properties of asymmetric coupled lines built in a 0.25 μ m CMOS technology in the frequency range of 50 MHz to 26.5 GHz. We show that the frequency-dependent line parameters extracted from calibrated four-port S-parameter measurements agree well with data predicted by numerical calculations. To our knowledge these are the first complete high-frequency measurements of the line parameters for asymmetric coupled lines on silicon ever reported.

[Return to main document.](#)